

CA Design

Cadence Allegro Librarian

1 day

1 – Introduction

A – Discuss the process of Libraries

2 – Part Requirements

A – Pins (Padstacks)

B - Assembly Outline

C – Silkscreen Outline

D – Reference Designators – Silkscreen and Assembly

E – Package Boundary (Height)

F – No-Probe Boundary

G – Misc other items

Keep-outs, pin numbering, polarization, special solder masks, special solder pastes.

3 – Sample part 1 – CAD01

Part exercise 1

4 – Sample part 2- CAD02

Part exercise 2

5 – Sample part 3 – Kycon Connector

Part exercise 3

6 – Sample Part 4 – Student selection.

Students will pick a “hard” part.